

# Computer Networks

## Exercise Session 08

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# General Schedule

All exercises will follow this general schedule

- Identify potential understanding problems
  - Ask your questions
  - Recap of the lecture
- Address the understanding problems
  - Answer your questions
  - Repeat certain topics
- Walk through the exercises/solutions → Some hints and guidance
  - Work time or presentation of results

# Network Layer: Addressing

You have seen ...

- the **purpose** and **format** of **IPv4** and **IPv6** addresses
- the original **classes** of IPv4 networks, what **CIDR** and what **subnets** are
- how to connect **private networks** to the Internet using **NAT**
- that IP datagrams can be **fragmented** if they are too big for a single frame on the data link layer

# Exercise 1: Error Control

- 1** An upper-layer packet is split into 16 frames, each of which has a 75 percent chance of arriving undamaged. If no error control is done by the data link protocol, how many frames are required to be sent on average to get the entire thing through?

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- 1** An upper-layer packet is split into 16 frames, each of which has a 75 percent chance of arriving undamaged. If no error control is done by the data link protocol, how many frames are required to be sent on average to get the entire thing through?
- The probability for the first frame to arrive error-free at the destination is  $75\% = 0.75$
  - The probability that both of the first two frames are received without an error is  $0.75 * 0.75 = 0.5625 = 56.25\%$
  - The probability that all first three frames arrive error-free is  $0.75 * 0.75 * 0.75 = 0.75^3 = 42.1875\%$
- ⇒ the probability that all  $n = 16$  frames arrive undamaged is  $p = 0.75^{16} \approx 1\%$
- $E = n * \frac{1}{p} \approx 16 * 99.77 \approx 1596$



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- The probability for the first frame to arrive error-free at the destination is 75 % = 0.75
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- ⇒ the probability that all  $n = 16$  frames arrive undamaged is  $p = 0.75^{16} \approx 1 %!$
- $E = n * \frac{1}{p} \approx 16 * 99.77 \approx 1596$

$$E = \sum_{i=1}^{\infty} ip(1-p)^{(i-1)}$$

$$= p \sum_{i=1}^{\infty} i(1-p)^{(i-1)}$$

Geometric series

$$S = \sum_{i=1}^{\infty} a^i = \frac{1}{1-\alpha}$$

$$\Rightarrow \sum_{i=1}^{\infty} i\alpha^{(i-1)} = \frac{1}{(1-\alpha)^2}$$

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$$E = \sum_{i=1}^{\infty} ip(1-p)^{(i-1)}$$
$$= p \sum_{i=1}^{\infty} i(1-p)^{(i-1)}$$

Substitute  $\alpha$  with  $(1-p)$

$$\Rightarrow E = \frac{1}{p} \Rightarrow \frac{1}{0.01} \approx 99.77$$

Geometric series

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- 4 Given the following valid codewords on the data link layer:

- $w_1 = 0001 \ 1111$
- $w_2 = 0111 \ 1111$
- $w_3 = 1100 \ 1111$
- $w_4 = 1011 \ 1111$
- $w_5 = 0001 \ 0000$
- $w_6 = 0111 \ 0000$
- $w_7 = 1100 \ 0000$
- $w_8 = 1011 \ 0000$

What is the minimum Hamming distance of this code? How many flipped bits could be detected? How many of them could be automatically be corrected?



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The CRC is computed during transmission and appended to the output stream as soon as the last bit goes out onto the wire. If the CRC were in the header, it would be necessary to make a pass over the frame to compute the CRC before transmitting. This would require each byte to be handled twice—once for checksumming and once for transmitting. Using the trailer cuts the work in half.



## Exercise 1: Error Control

- 6 For the data 0xDE 0xAD 0xBE 0xEF the *CRC16-CCITT* results in 0x19 0x15. Which of the following blocks of data will certainly result in a different *CRC16-CCITT* checksum?
- 0xDE 0xAD 0xBE 0xFF
  - 0xDE 0xAD 0xBE 0xE8
  - 0xFF 0xFD 0xBE 0xEF
  - 0x9E 0xAD 0xBE 0xED
  - 0xDE 0xAD 0xBE 0xD0

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1 bit error → can be detected by *CRC16-CCITT*
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3 bit error → can be detected by *CRC16-CCITT*
  - 0xFF 0xFD 0xBE 0xEF
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  - 0xDE 0xAD 0xBE 0xD0

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4 bit error → may not be detected by *CRC16-CCITT*
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  - 0xFF 0xFD 0xBE 0xEF  
4 bit error → may not be detected by *CRC16-CCITT*
  - 0x9E 0xAD 0xBE 0xED  
2 bit error → can be detected by *CRC16-CCITT*
  - 0xDE 0xAD 0xBE 0xD0

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3 bit error → can be detected by *CRC16-CCITT*
  - 0xFF 0xFD 0xBE 0xEF  
4 bit error → may not be detected by *CRC16-CCITT*
  - 0x9E 0xAD 0xBE 0xED  
2 bit error → can be detected by *CRC16-CCITT*
  - 0xDE 0xAD 0xBE 0xD0  
burst error with less than 16 bits → can be detected by *CRC16-CCITT*

## Exercise 2: Error Detection – CRC

- 1 Generator polynomial: 100101  
Payload: 11010011

# Exercise 2: Error Detection – CRC

**1** Generator polynomial: 100101

Payload: 11010011

The generator polynomial has 6 digits  $\implies$  five 0 bits are appended

Frame with appended 0 bits: 1101001100000

```
1101001100000
```

```
100101|||||
```

```
-----v|||||
```

```
100011|||||
```

```
100101|||||
```

```
-----vvv|||
```

```
110100|||
```

```
100101|||
```

```
-----v||
```

```
100010||
```

```
100101||
```

```
-----vv
```

11100 = Remainder

Remainder: 11100

Transferred frame: **1101001111100**



## Exercise 2: Error Detection – CRC

- 2 Transferred frame: 1101001110100  
Generator polynomial: 100101

## Exercise 2: Error Detection – CRC

- 2 Transferred frame: 1101001110100  
 Generator polynomial: 100101

```

1101001110100
100101|||||
-----v|||||
 100011|||||
 100101|||||
-----vvv|||
   110110|||
   100101|||
-----v||
    100111||
    100101||
-----vv
      1000 => Error
  
```

## Exercise 2: Error Detection – CRC

- 3** Transferred frame: 1101001111100  
Generator polynomial: 100101

## Exercise 2: Error Detection – CRC

**3** Transferred frame: 1101001111100  
 Generator polynomial: 100101

```
1101001111100
```

```
100101|||||
```

```
-----v|||||
```

```
100011|||||
```

```
100101|||||
```

```
-----vvv|||
```

```
110111|||
```

```
100101|||
```

```
-----v||
```

```
100101||
```

```
100101||
```

```
-----vv
```

```
00 => Transmission was error-free
```

## Exercise 2: Error Detection – CRC

- 4 Generator polynomial: 100101  
Payload: 10110101

## Exercise 2: Error Detection – CRC

4 Generator polynomial: 100101

Payload: 10110101

The generator polynomial has 6 digits  $\implies$  five 0 bits are appended.

Frame with appended 0 bits: 1011010100000

```
1011010100000
```

```
100101||||||
```

```
-----vv||||
```

```
  100001||||
```

```
  100101||||
```

```
-----vv|||
```

```
  100000||
```

```
  100101||
```

```
-----vv
```

```
  10100 = Remainder
```

Remainder: 10100

Transferred frame: **1011010110100**

## Exercise 2: Error Detection – CRC

- 5 Transferred frame: 1011010110110  
Generator polynomial: 100101

## Exercise 2: Error Detection – CRC

5 Transferred frame: 1011010110110

Generator polynomial: 100101

1011010110110

100101|||||

-----vv||||

100001|||||

100101|||||

-----vvv||

100101||

100101||

-----vv

10 => Error



## Exercise 2: Error Detection – CRC

- 6 Transferred frame: 1011010110100  
Generator polynomial: 100101

## Exercise 2: Error Detection – CRC

6 Transferred frame: 1011010110100

Generator polynomial: 100101

1011010110100

100101|||||

-----vv||||

100001|||||

100101|||||

-----vvv||

100101||

100101||

-----vv

00 => Transmission was error-free



## Exercise 2: Error Detection – CRC

- 7 Transferred frame: 1010010110100  
Generator polynomial: 100101

## Exercise 2: Error Detection – CRC

- 7** Transferred frame: 1010010110100  
 Generator polynomial: 100101

```

1010010110100
100101|||||
-----v||||
  110001||||
  100101||||
  -----v|||
    101001|||
    100101|||
    -----v||
      110001||
      100101||
      -----v|
        101000|
        100101|
        -----v
          11010 => Error
  
```

## Exercise 2: Error Detection – CRC

- 8 Generator polynomial: 100000111  
Payload: 1101010101110101

# Exercise 2: Error Detection – CRC

**8** Generator polynomial: 100000111

Payload: 1101010101110101

The generator polynomial has 9 digits

⇒ eight 0 bits are appended.

Frame with appended 0 bits:

11010101011101010000000

Remainder: 10110111

Transferred frame:

110101010111010110110111

```

11010101011101010000000
100000111| | | | | | | | | |
-----v| | | | | | | | | |
101011011| | | | | | | | | |
100000111| | | | | | | | | |
-----vv| | | | | | | | | |
101110011| | | | | | | | | |
100000111| | | | | | | | | |
-----vv| | | | | | | | | |
111010001| | | | | | | | | |
100000111| | | | | | | | | |
-----v| | | | | | | | | |
110101100| | | | | | | | | |
100000111| | | | | | | | | |
-----v| | | | | | | | | |
101010111| | | | | | | | | |
100000111| | | | | | | | | |
-----vv| | | | | | | | | |
101000000| | | | | | | | | |
100000111| | | | | | | | | |
-----vv| | | | | | | | | |
100011100| | | | | | | | | |
100000111| | | | | | | | | |
-----vvv| | | | | | | | | |
110110000
100000111
-----
10110111 = Remainder

```



## Exercise 2: Error Detection – CRC

- 9 Transferred frame: 110101010111110110110111  
Generator polynomial: 100000111

# Exercise 2: Error Detection – CRC

- 9 Transferred frame: 110101010111110110110111  
Generator polynomial: 100000111

```

110101010111110110110111
100000111|
-----v|
101011011|
100000111|
-----vv|
      101110011
      100000111
      -----
            111010011
            100000111
            -----
            110101000
            100000111
            -----
            101011111
            100000111
            -----
            101100010
            100000111
            -----
            110010111
            100000111
            -----
            100100000
            100000111
            -----
            100111111
            100000111
            -----
            111000 => Error

```



## Exercise 2: Error Detection – CRC

**10** Transferred frame: 110101010111010110110111  
Generator polynomial: 100000111

# Exercise 2: Error Detection – CRC

**10** Transferred frame: 110101011101011011011  
 Generator polynomial: 10000111

```

110101011101011011011
10000111|
-----v|
101011011|
10000111|
-----vv|
101110011|
10000111|
-----vv|
111010001|
10000111|
-----v|
110101100|
10000111|
-----v|
101010111|
10000111|
-----vv|
101000010|
10000111|
-----vv|
100010111|
10000111|
-----vvv
10000111
10000111
-----

```

0 => Transmission was error-free

## Exercise 3: Media Access Control

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In a deterministic MAC the data rate and latency for each station can be predicted because the resources are pre-allocated, a non-deterministic MAC follows a best-effort principle.

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Non-deterministic media access control
- 4** Which media access control method is implemented by **Token Ring**?

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Non-deterministic media access control
- 4** Which media access control method is implemented by **Token Ring**?  
Deterministic media access control
- 5** Which media access control method is implemented by **WLAN**?

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With wireless networks, it is not guaranteed that all stations can detect all collisions. In wired networks with a shared transmission medium, each participant receives the transmissions of all other participants.
- 8 How do Ethernet devices react, when they detect a **collision**?  
If a collision is detected, the sender stops the frame transmission and sends the jam signal to announce the collision. If the maximum number of transmission attempts is not yet reached, the sender tries to transmit the frame again after a random time.

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- 10 Explain what is done to ensure that the transmission of a frame is not completed when a collision occurs in an **Ethernet** network. Each frame must have a certain minimum length. It must be dimensioned in a way, that the transmission duration for a frame with minimum length does not fall below the maximum RTT (round trip time). This ensures that a collision reaches the sender before its transmission is finished. If a sender detects a collision, it knows that its frame has not arrived correctly at the receiver, and can try the transmission again later.

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- 11 Why is the MAC protocol less relevant for modern Ethernet networks?  
Modern Ethernet networks are typically switched, i.e., the stations do not share a transmission medium.

## Exercise 4: CSMA

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## Exercise 4: CSMA

- 1 Which prerequisite needs to be fulfilled to use a CSMA MAC protocol?  
Each station must be able to sense the medium.
- 2 For  $p$ -persistent CSMA the size of  $p$  determines the performance of the network. In which cases is preferable to use a higher value for  $p$ ?  
If the utilization is low and the latency is most important.

## Exercise 4: CSMA

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The ACK frame is used to tell the sender that the frame was successfully received, i.e., that no collision has occurred.

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The ARP cache is a table, which contains IP addresses and MAC addresses, that belong together. It is used to speed up the address resolution.

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Digital signatures describe a methods to prove the **authenticity** of a message by calculating a hash value over the message. The idea is similar to CRC checksums: any change to the message shall result in a different signature/checksum value. However, for an error detection algorithm it is of importance to require little computing time. For a digital signature it is most important that the reverse direction (from the hash to the message) is as expensive as possible.

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A so called *gratuitous ARP* can be used for duplicate address detection, to update the ARP caches of the other nodes, or to announce the existence of a node.